Title: Semiconductor Integrated Circuit Having Reduced Cross-Talk Noise

Inventor: K. Uyshiyama et al.
Application No. New Division
Docket No. 100353-00176

FIG. 1A

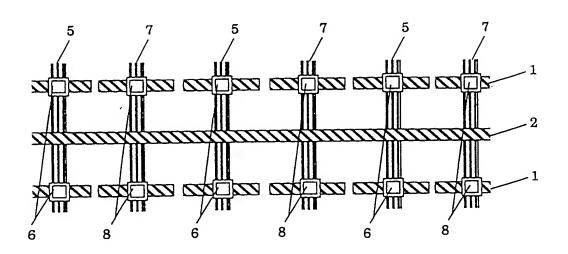
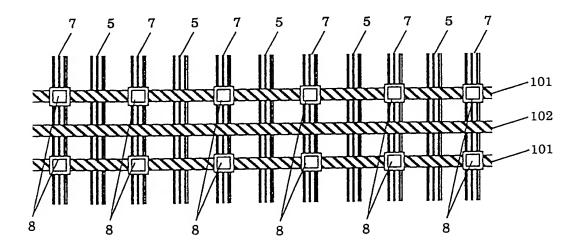


FIG. 1B



Title: Semiconductor Integrated Circuit Having Reduced Cross-Talk Noise Inventor: K. Uyshiyama et al. Application No. New Division Docket No. 100353-00176

FIG. 2

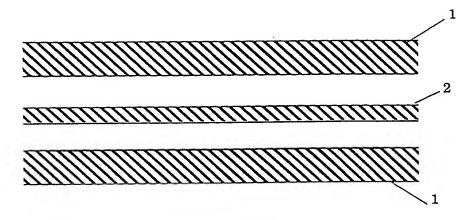
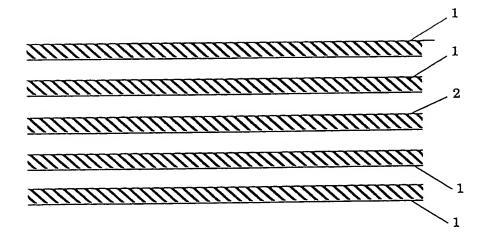


FIG. 3



Title: Semiconductor Integrated Circuit Having Reduced Cross-Talk Noise Inventor: K. Uyshiyama et al. Application No. New Division Docket No. 100353-00176

FIG. 4

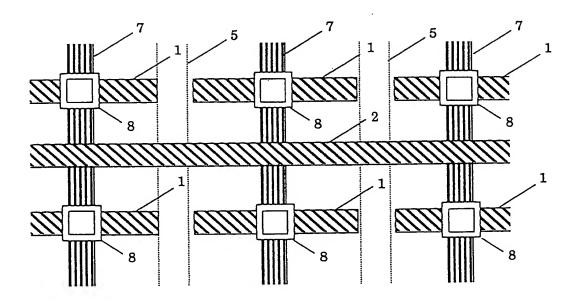
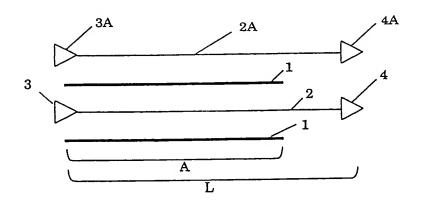


FIG. 5



Application No. New Division Docket No. 100353-00176

FIG. 6A

FIG. 6B

A Delay

FIG. 6C

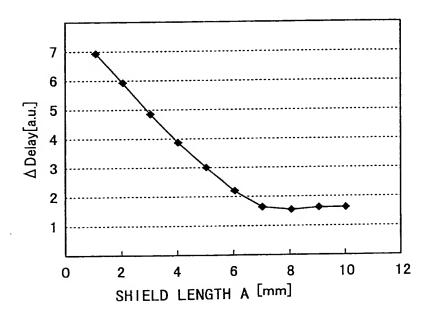
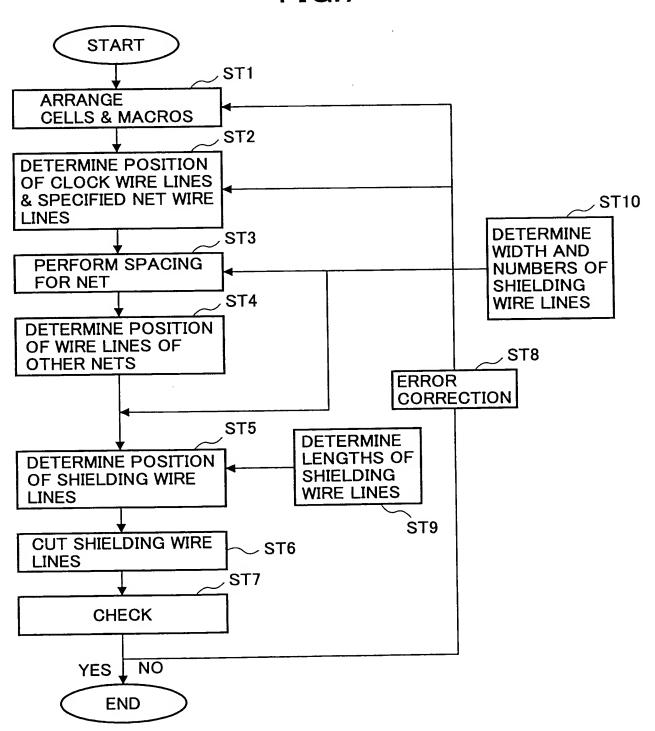
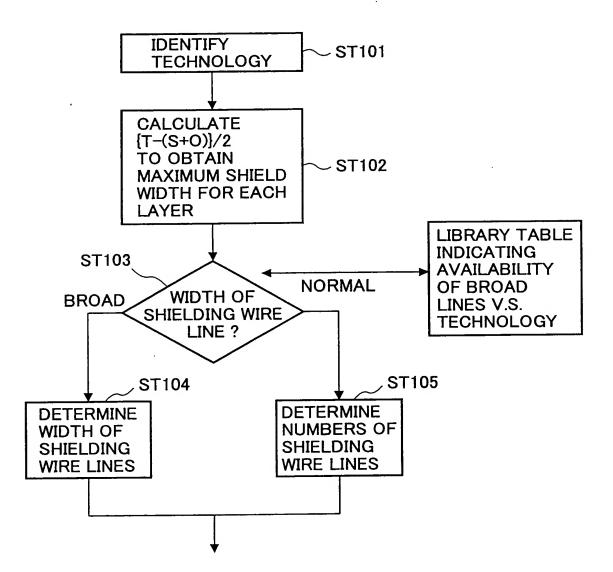


FIG.7



Title: Semiconductor Integrated Circuit Having Reduced Cross-Talk Noise Inventor: K. Uyshiyama et al. Application No. New Division Docket No. 100353-00176

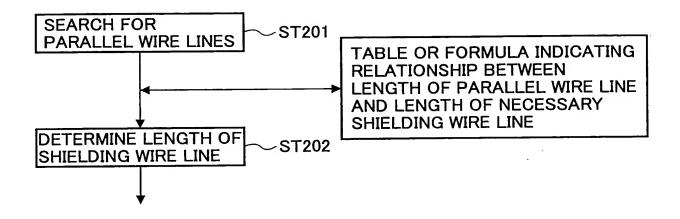
FIG.8



Title: Semiconductor Integrated Circuit Having

Reduced Cross-Talk Noise Inventor: K. Uyshiyama et al. Application No. New Division Docket No. 100353-00176

FIG.9



**FIG.10** 

		NECESSARY SHIELDING WIRE LINE						
LENGTH OF PARALLEL WIRE LINE		10	9	8	7	6	5	4
	10	85%	85%	85%	85%	75%	65%	
	9	•	•	•	•	•		_
	8	•	•	•	•	•		
	•	•	•					
	•					İ		